

FIGURE 2

FIG. 3

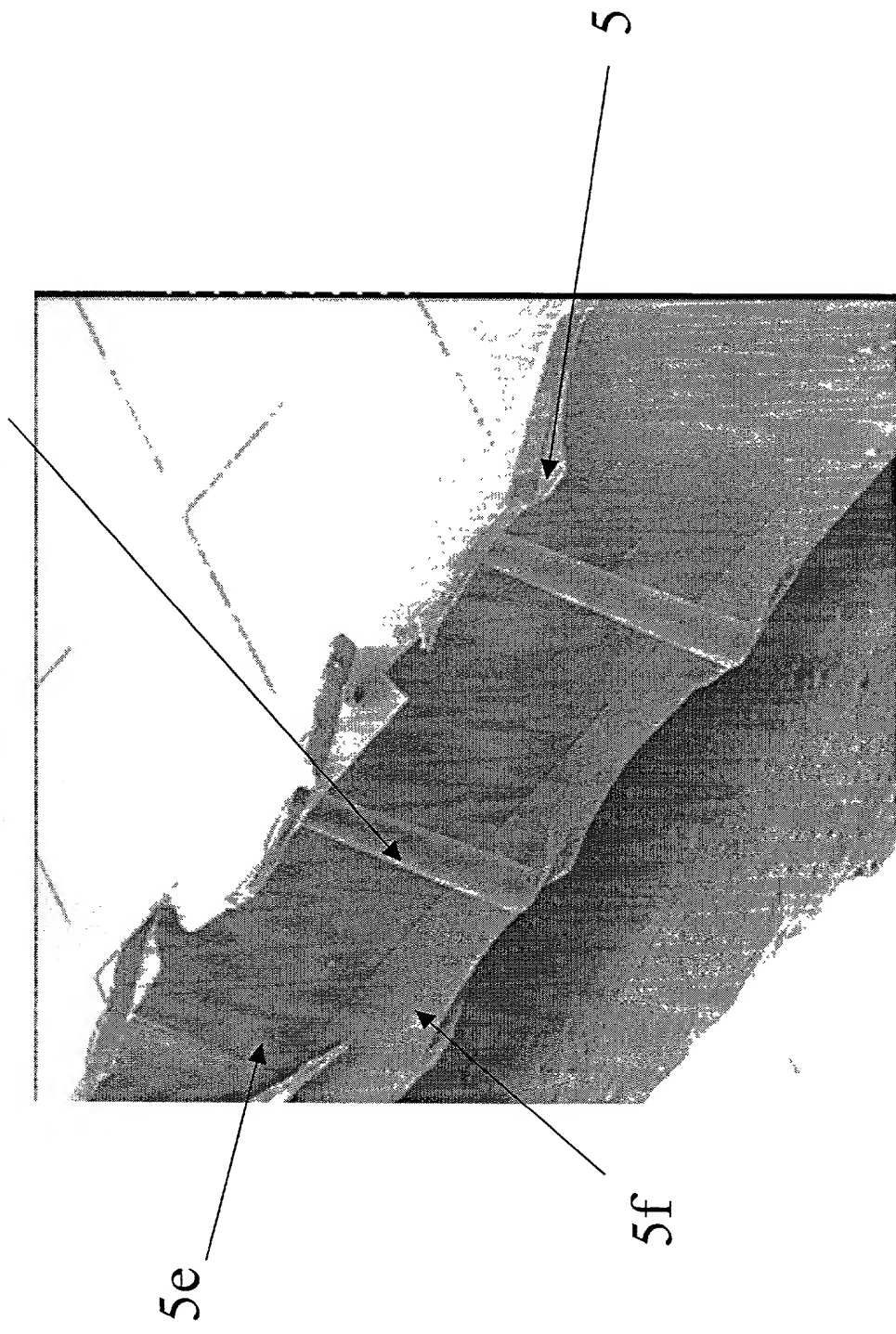


FIGURE 3

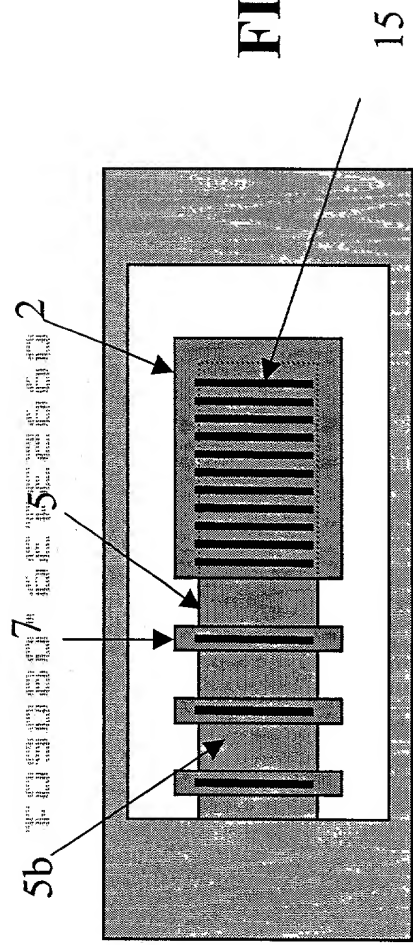


FIGURE 4(a)

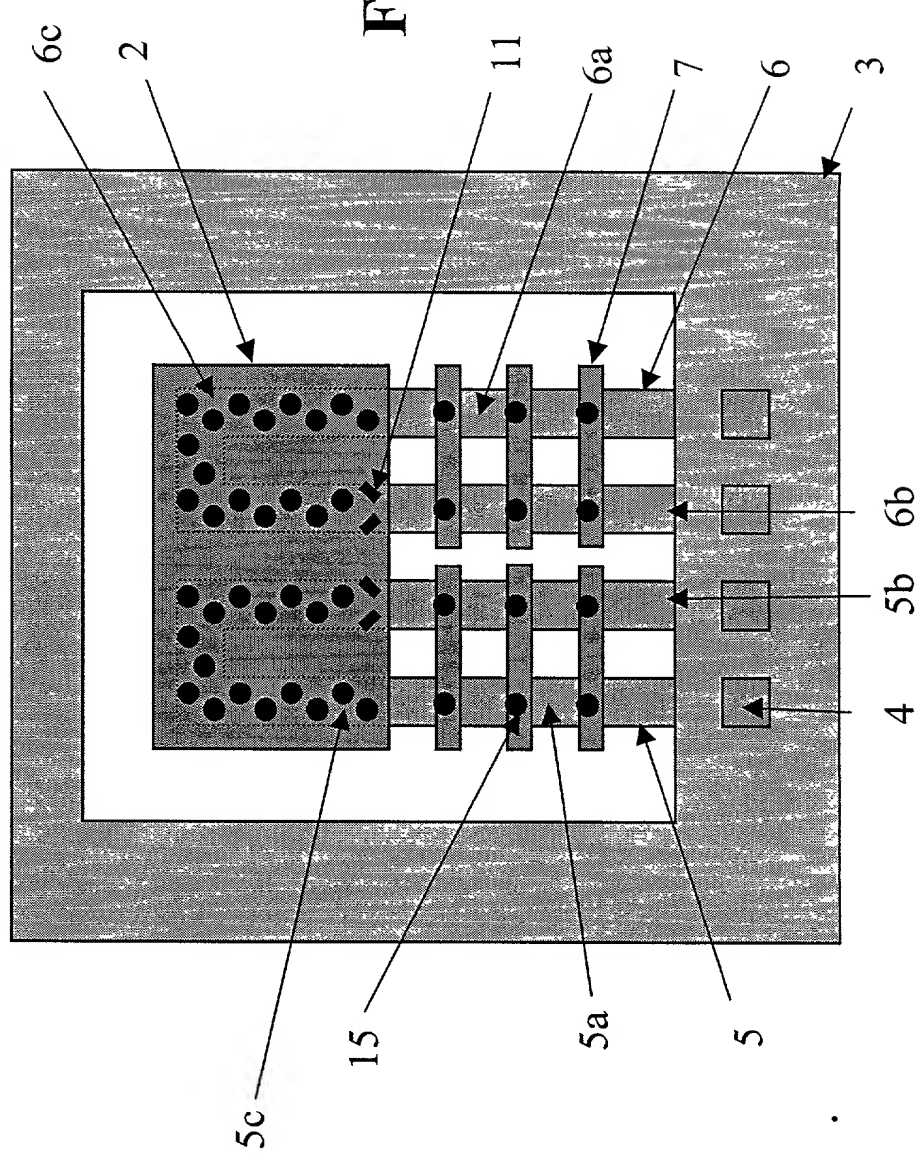
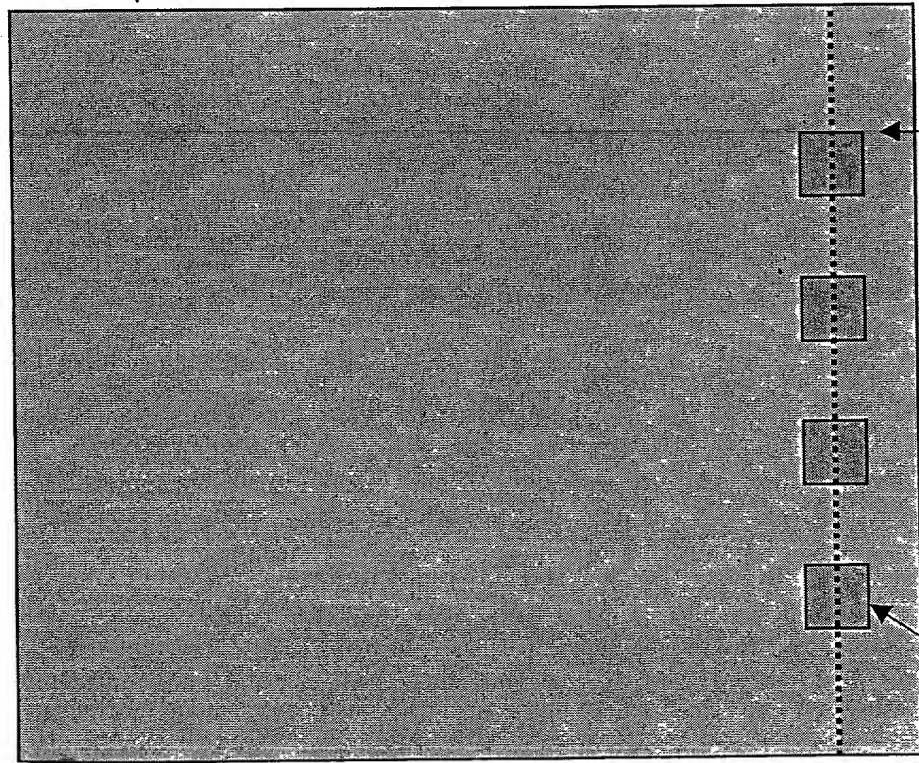


FIGURE 4(b)



104 **FIGURE 5(a)** 103

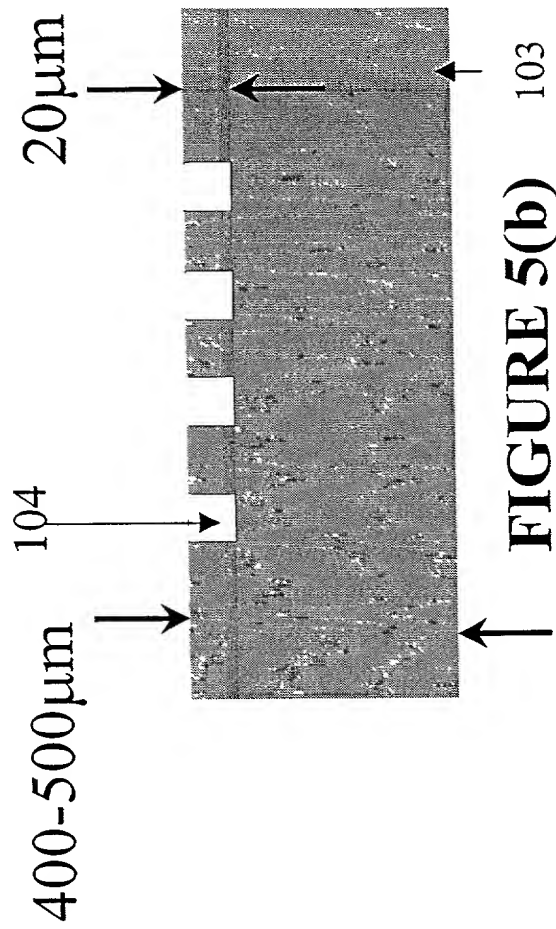


FIGURE 5(b) 103

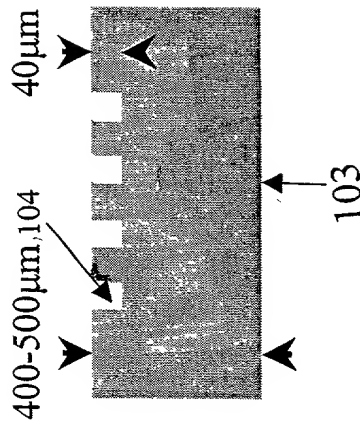
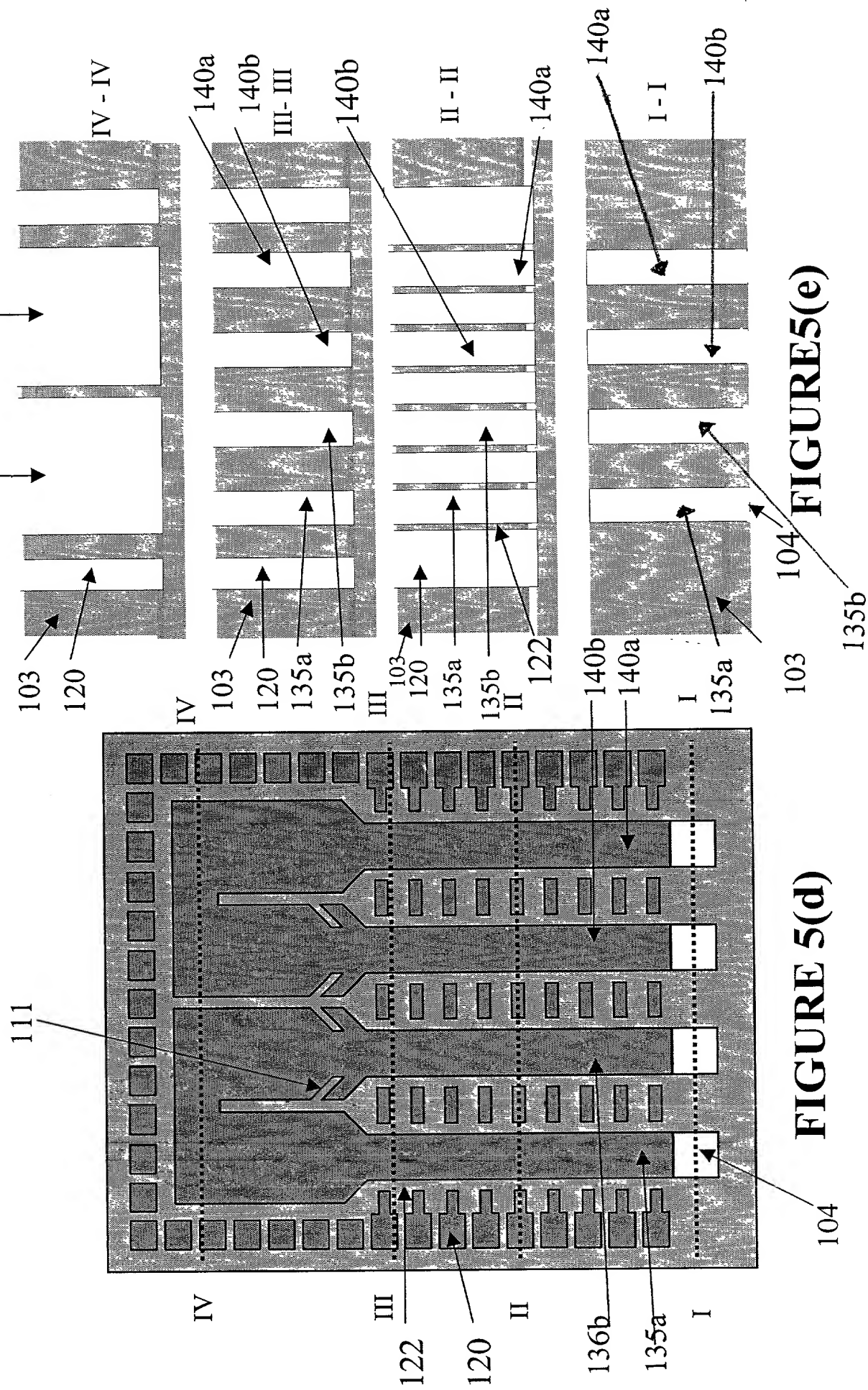


FIGURE 5(c) 103



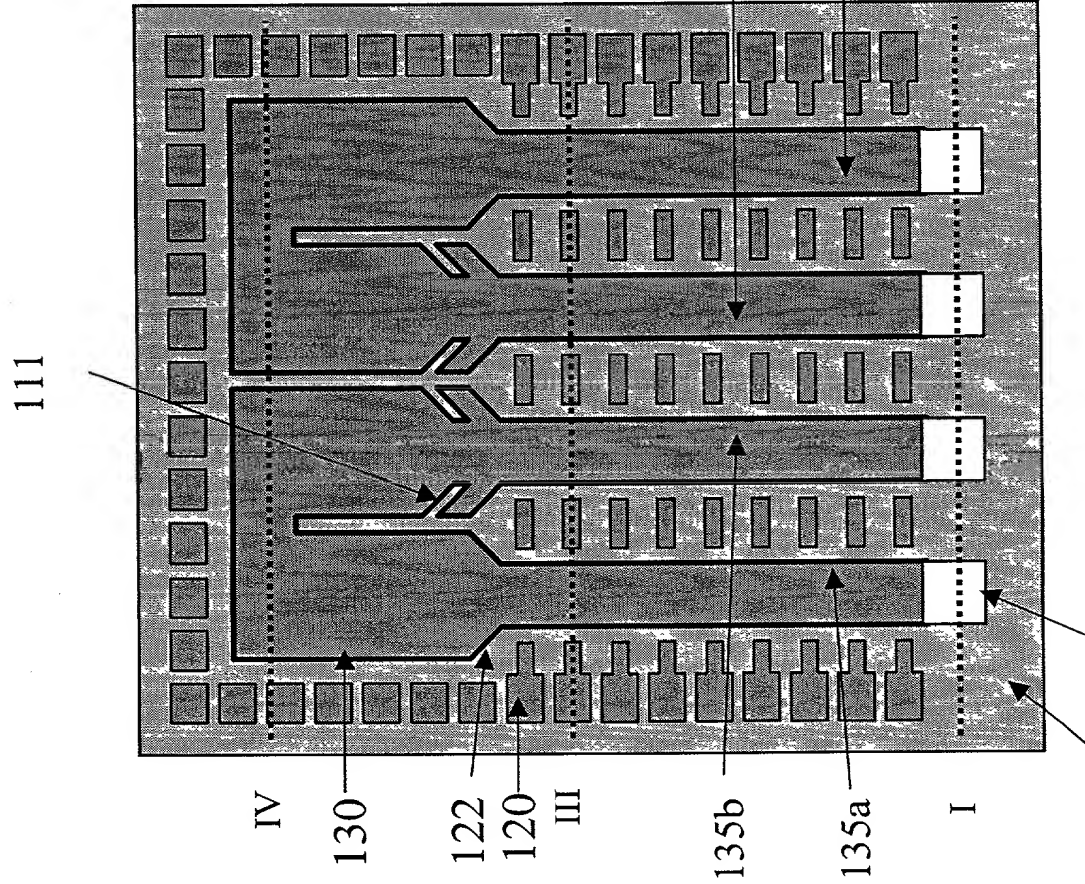


FIGURE 5(f)

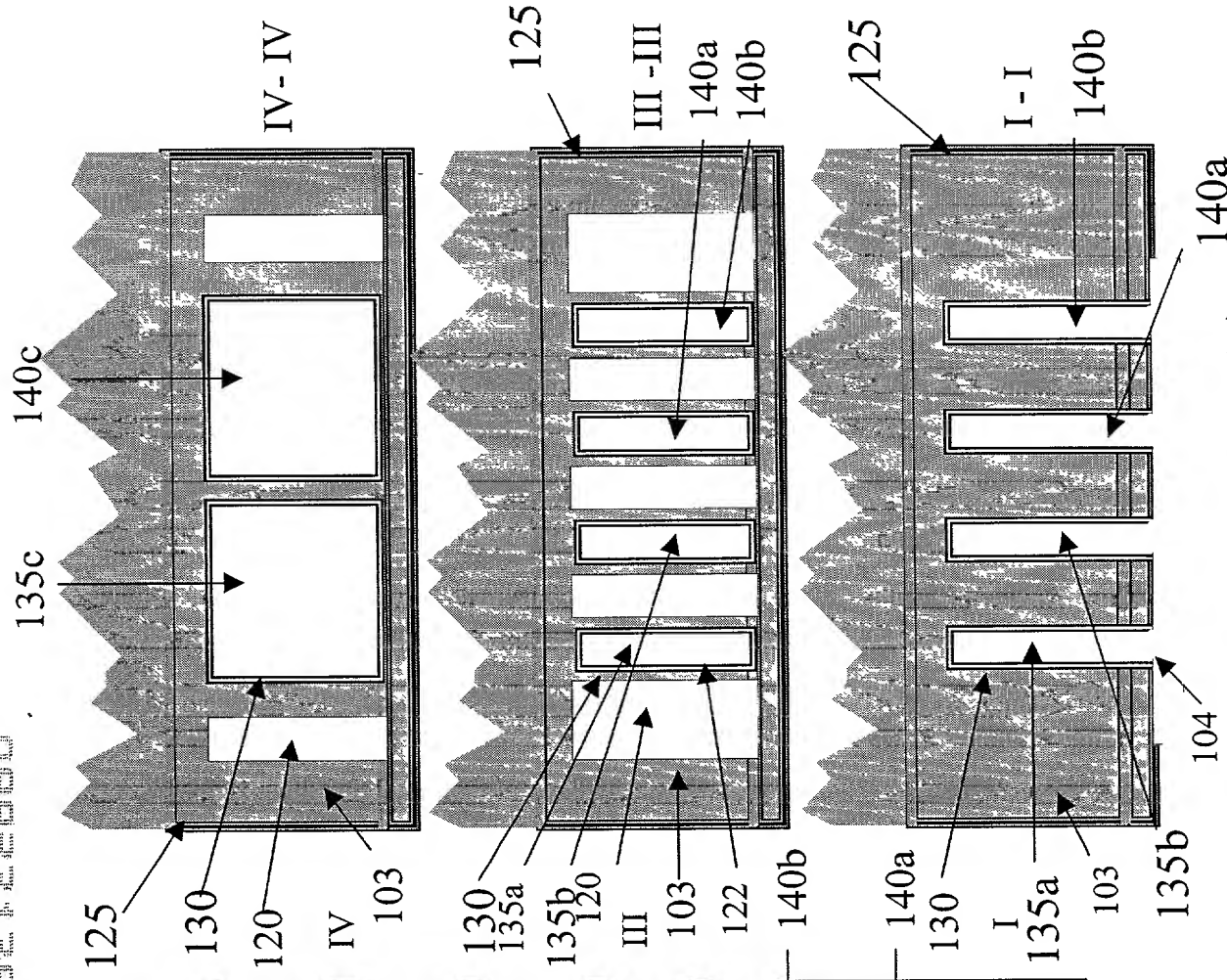


FIGURE 5(g)

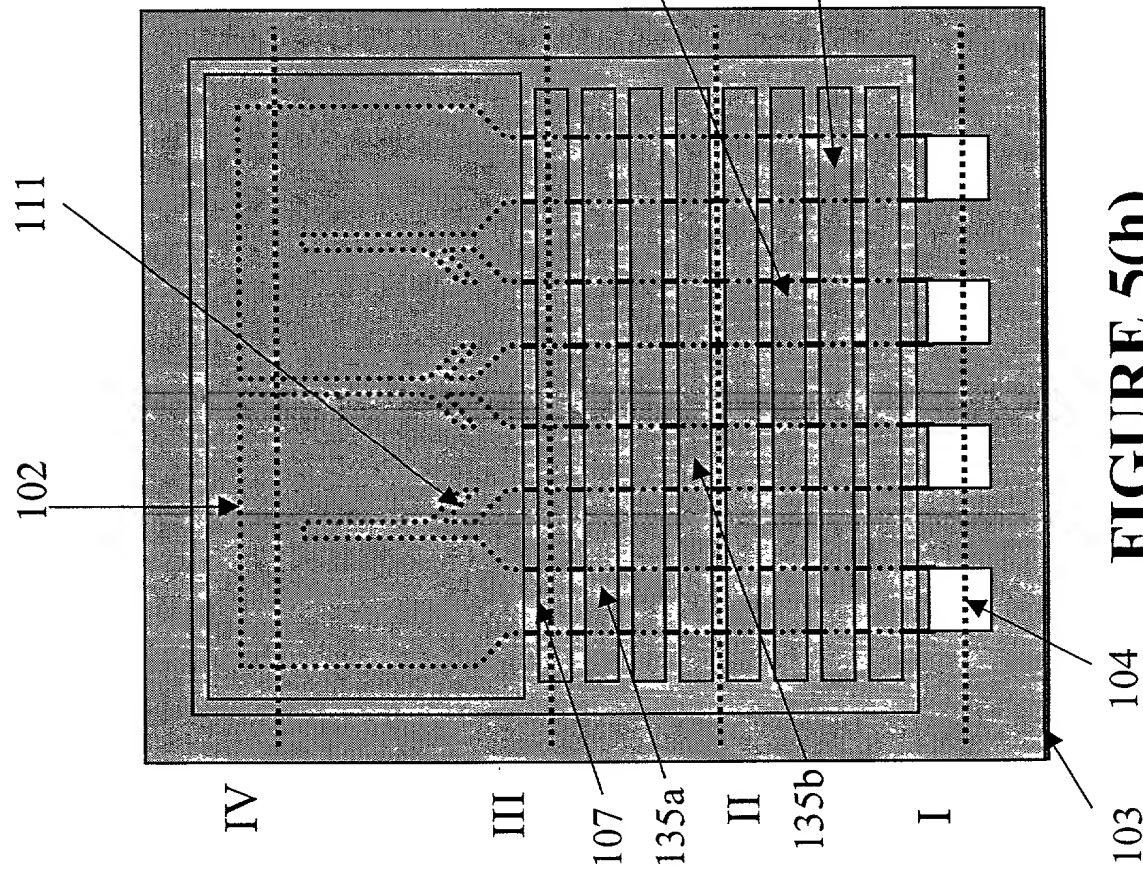


FIGURE 5(h)

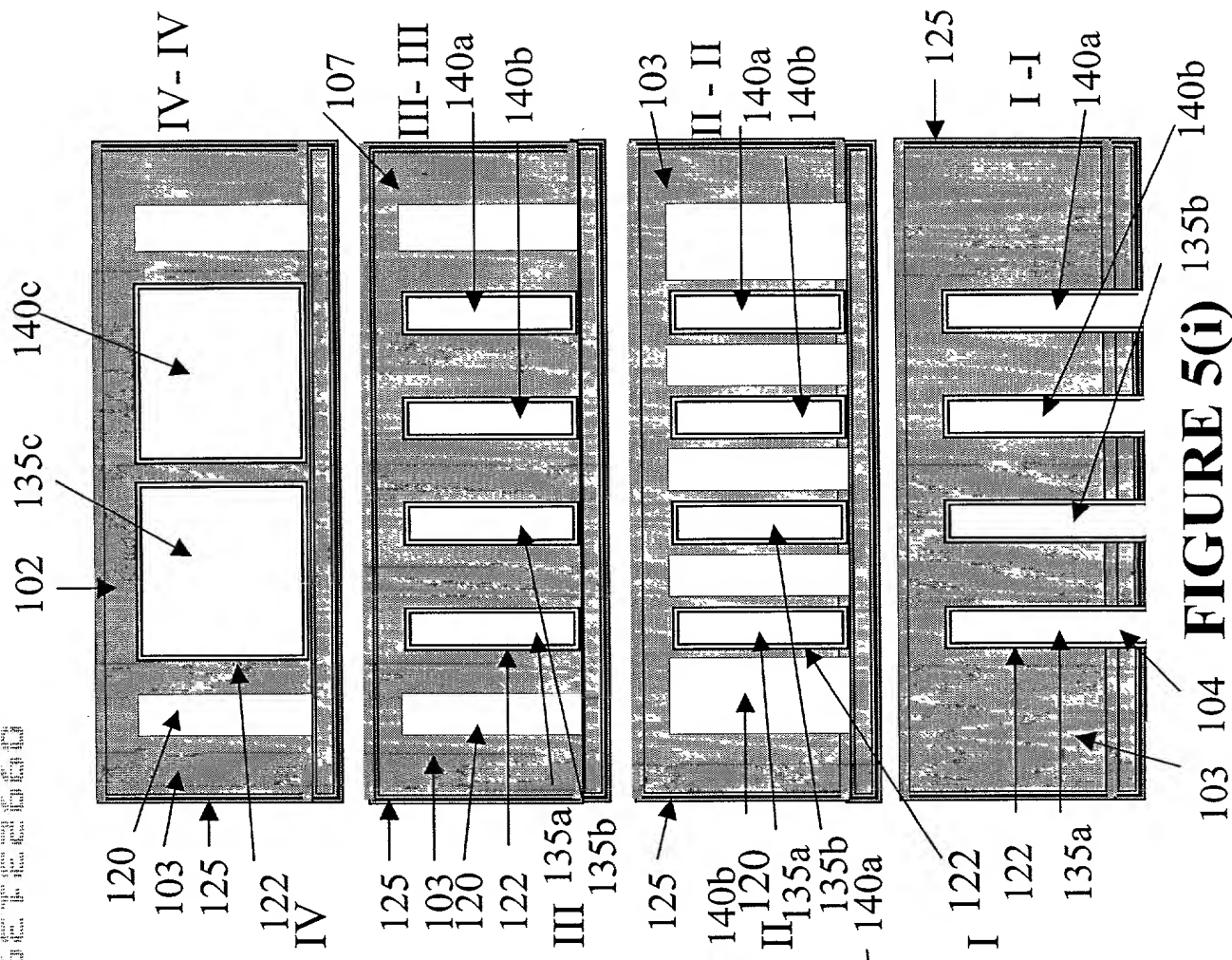


FIGURE 5(i)

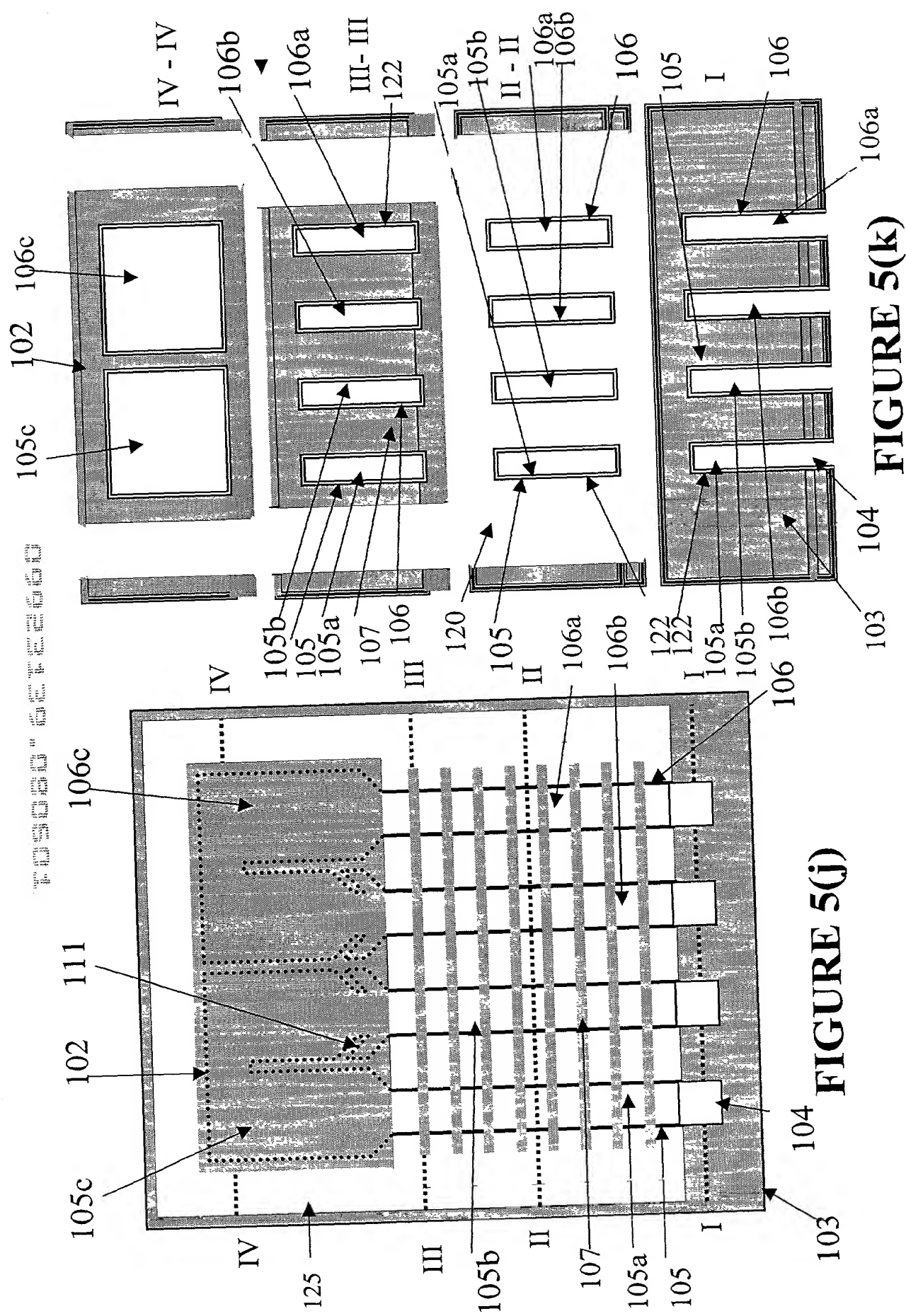
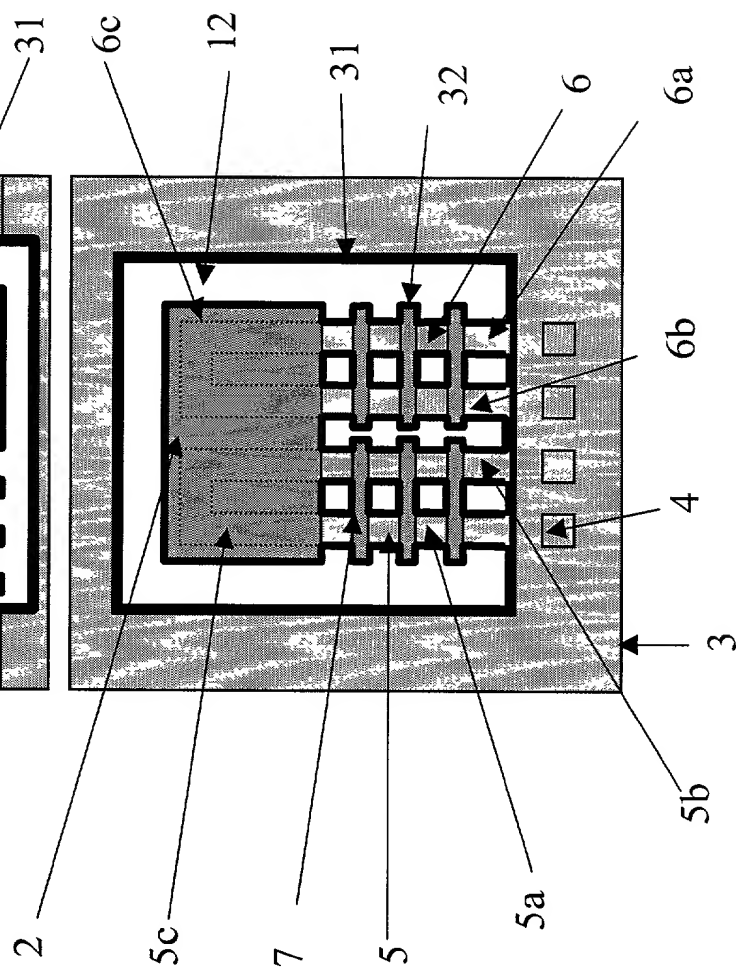
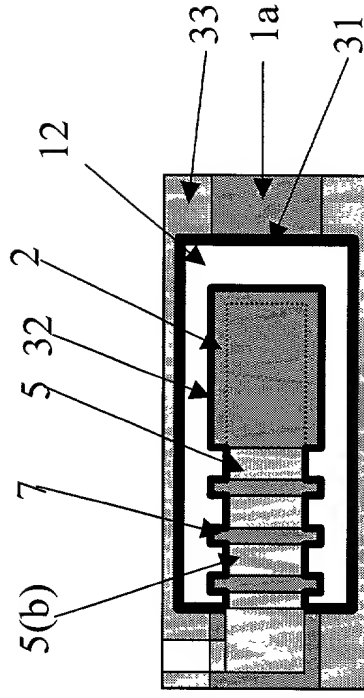


FIGURE 5(j)

FIGURE 5(k)



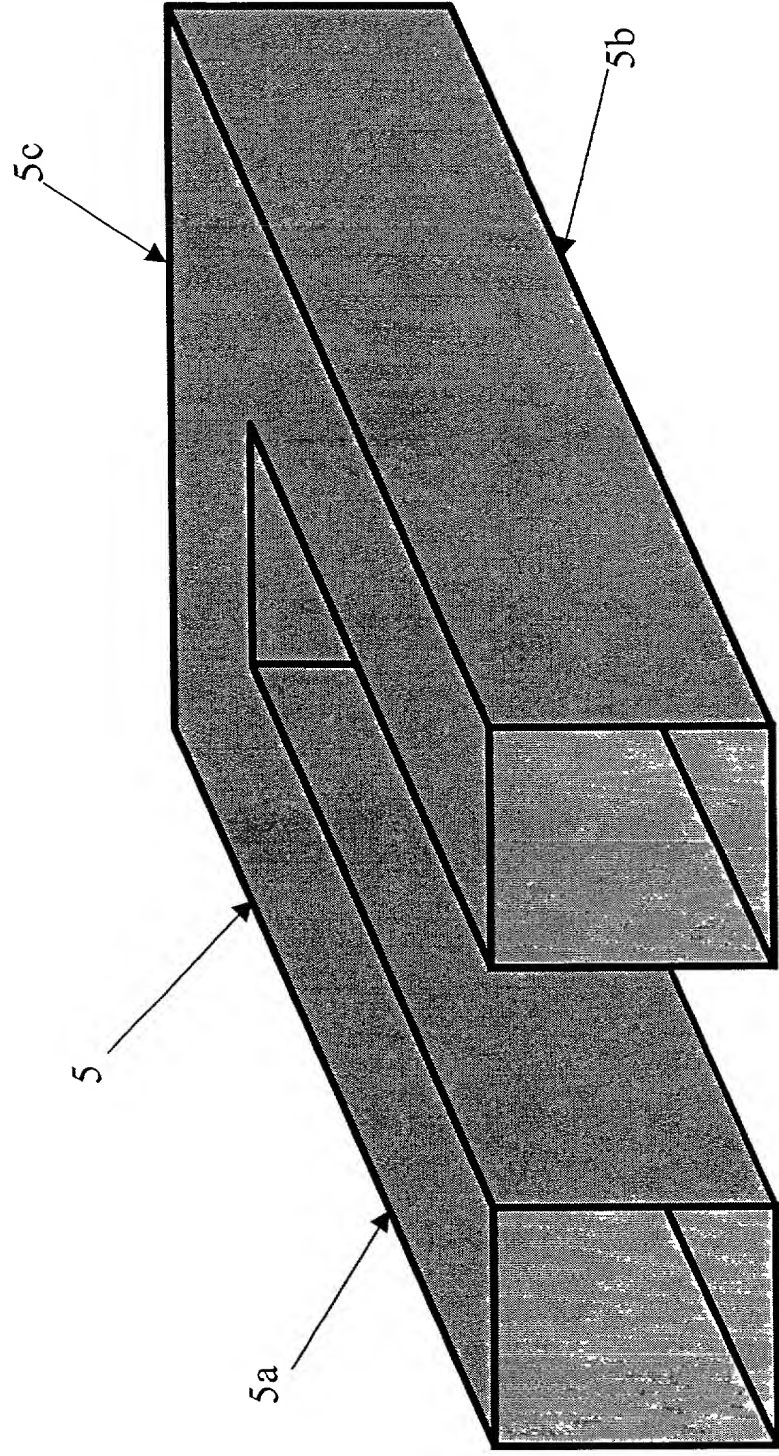
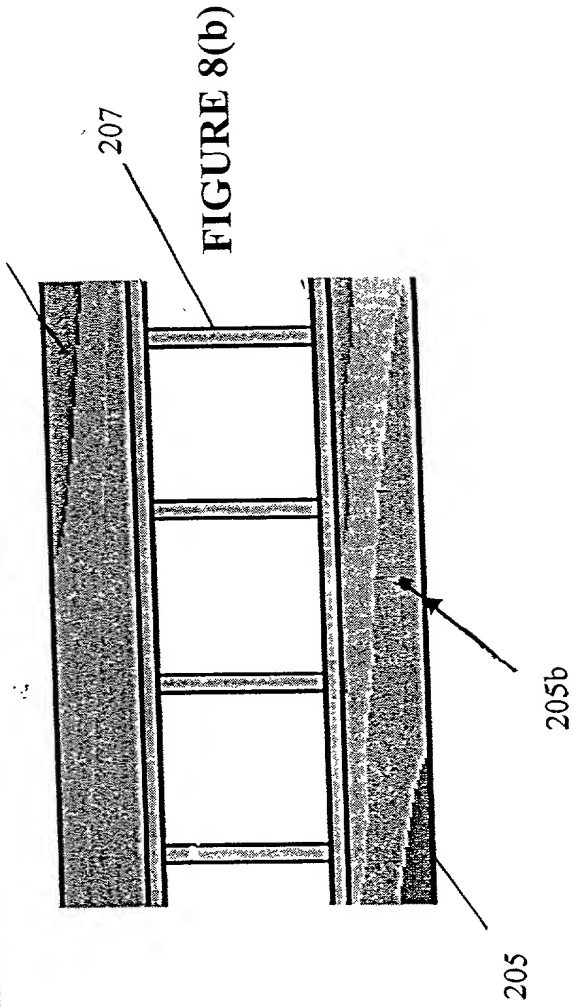
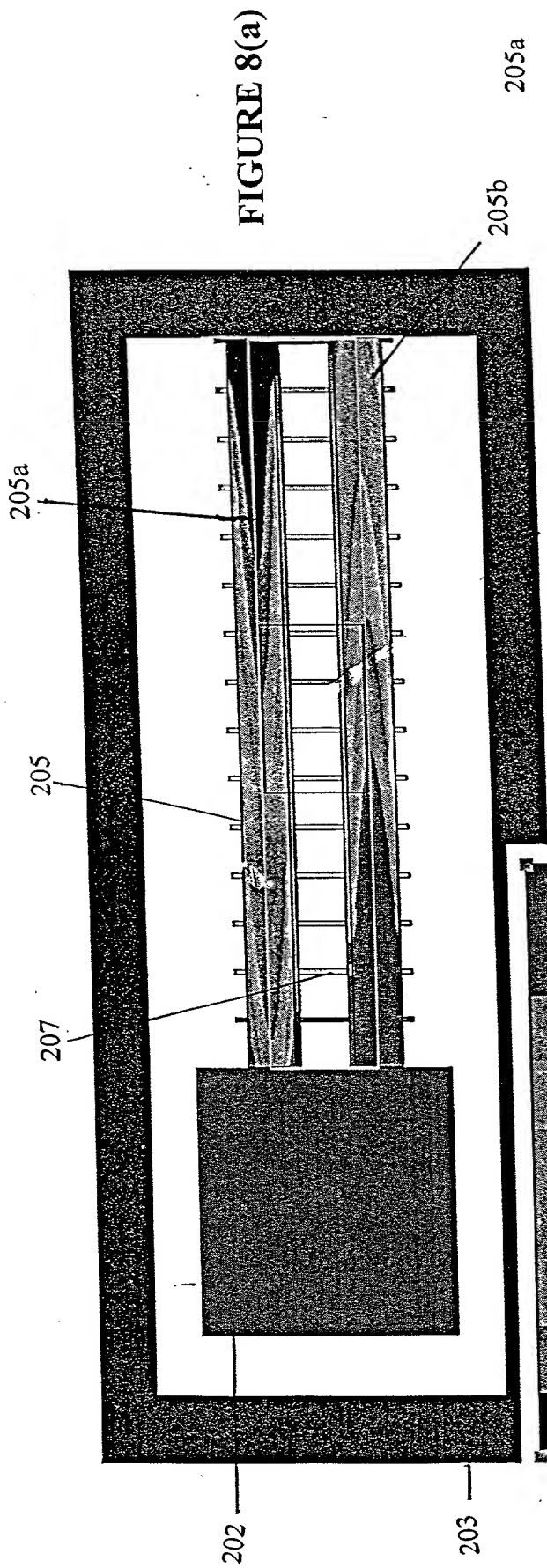
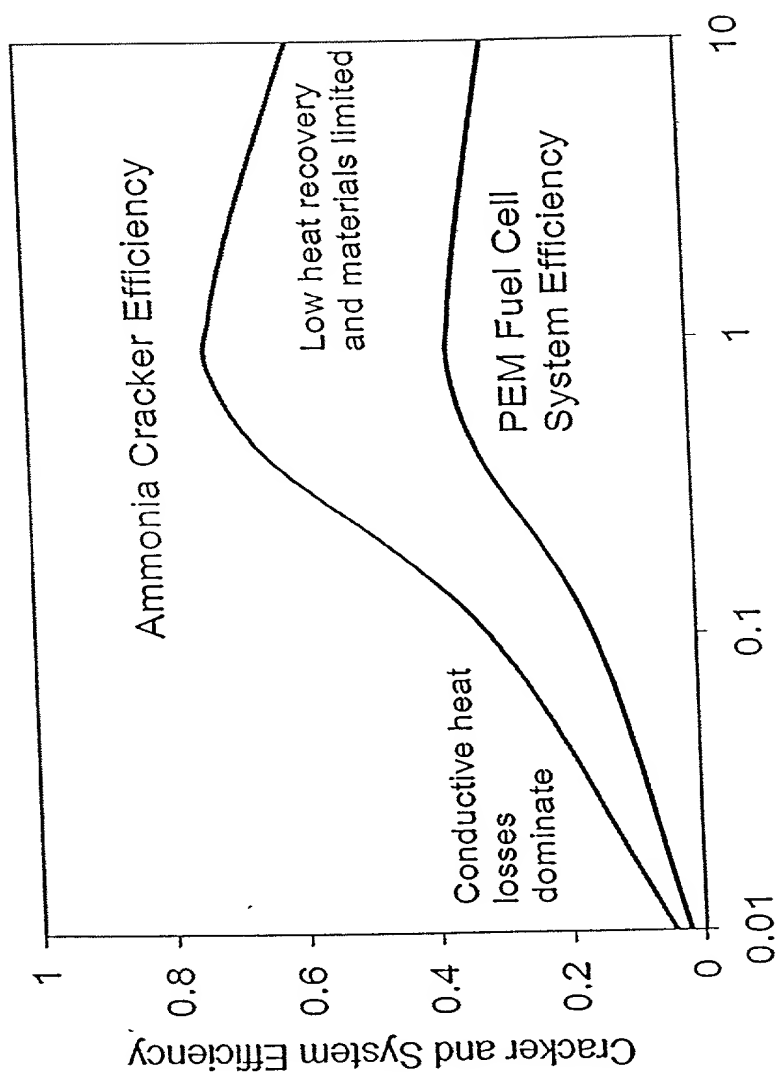


FIGURE 7





System Power Output (w)

FIGURE 9

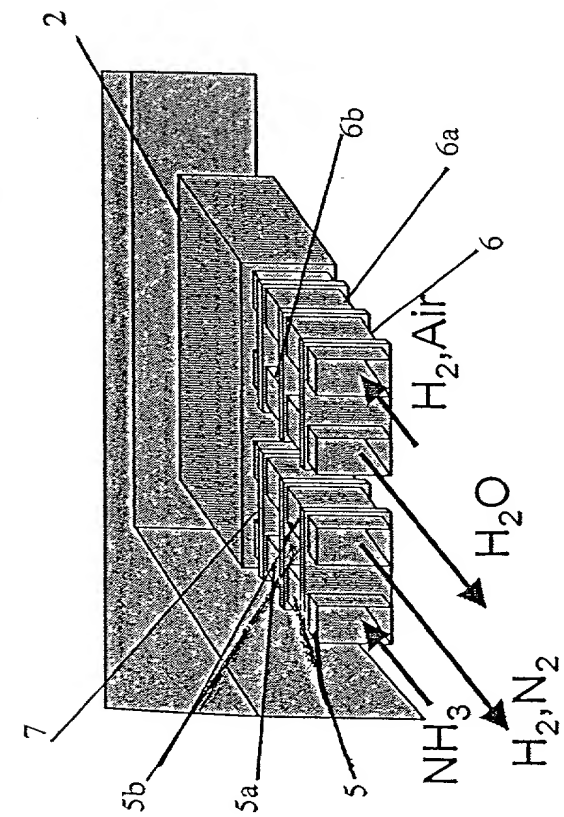


FIGURE 10(a)

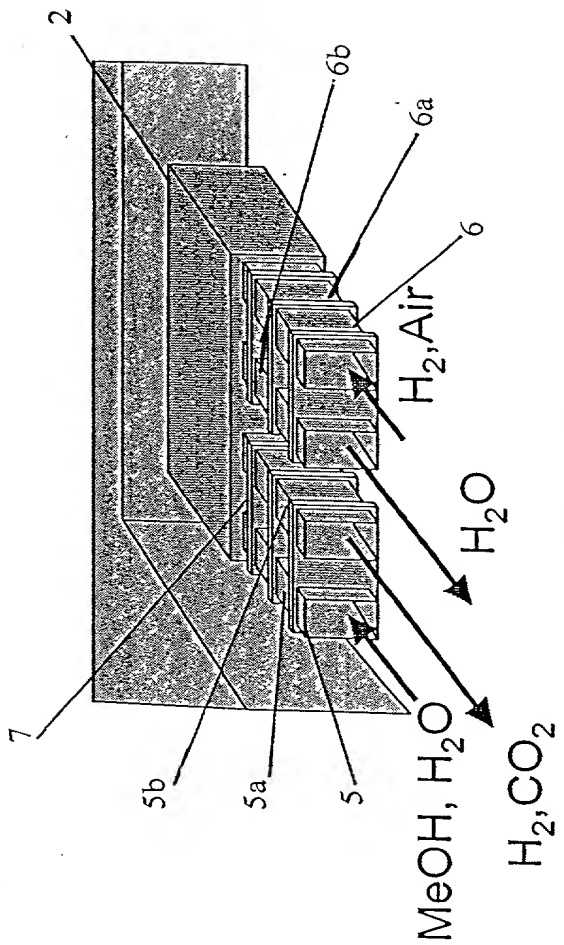


FIGURE 10(b)

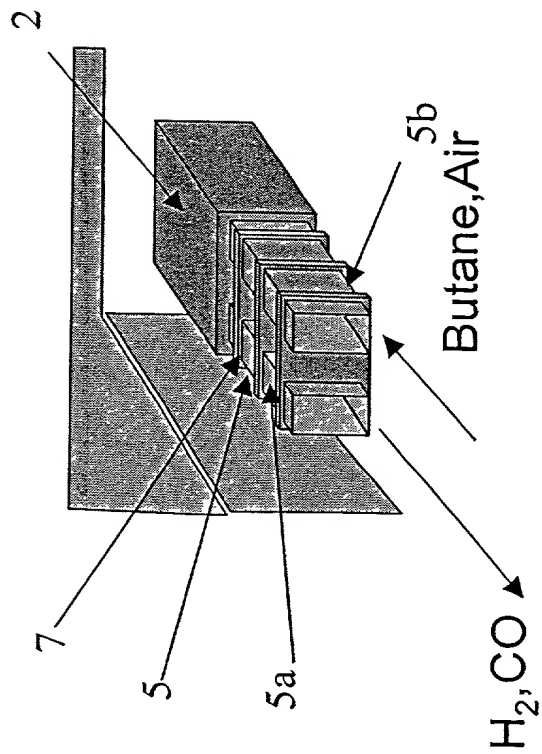


FIGURE 10(c)

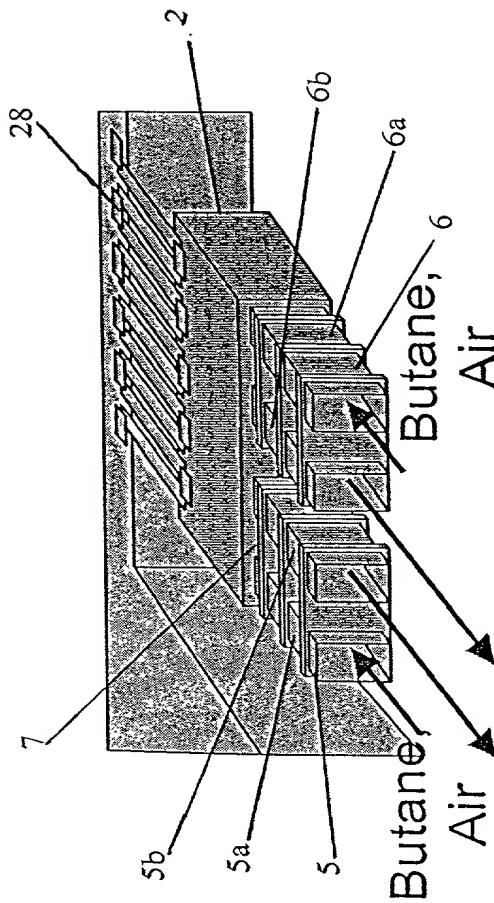


FIGURE 10(d)

CO₂

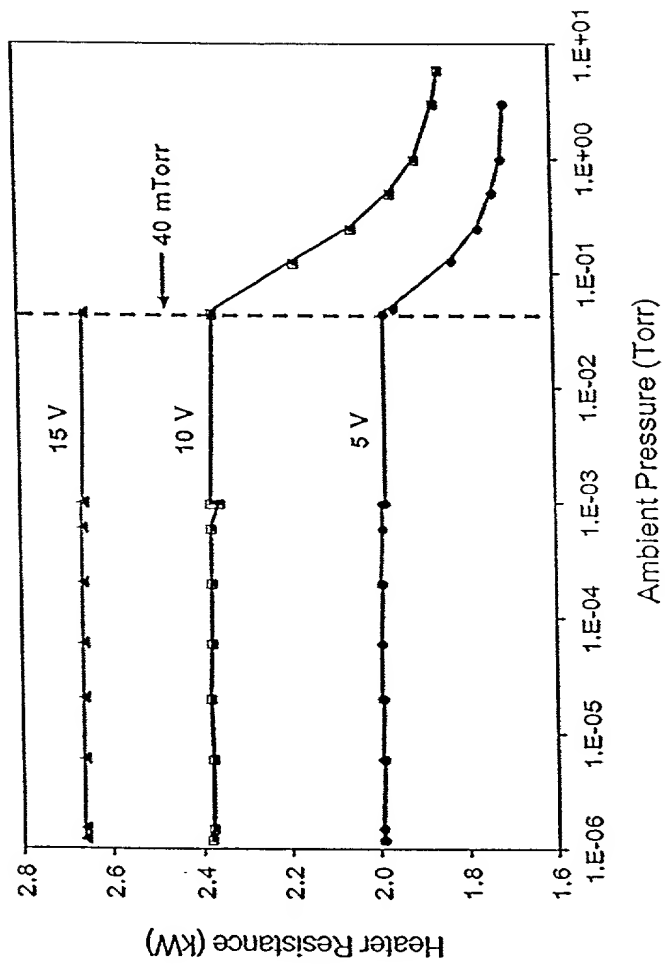


FIGURE 11

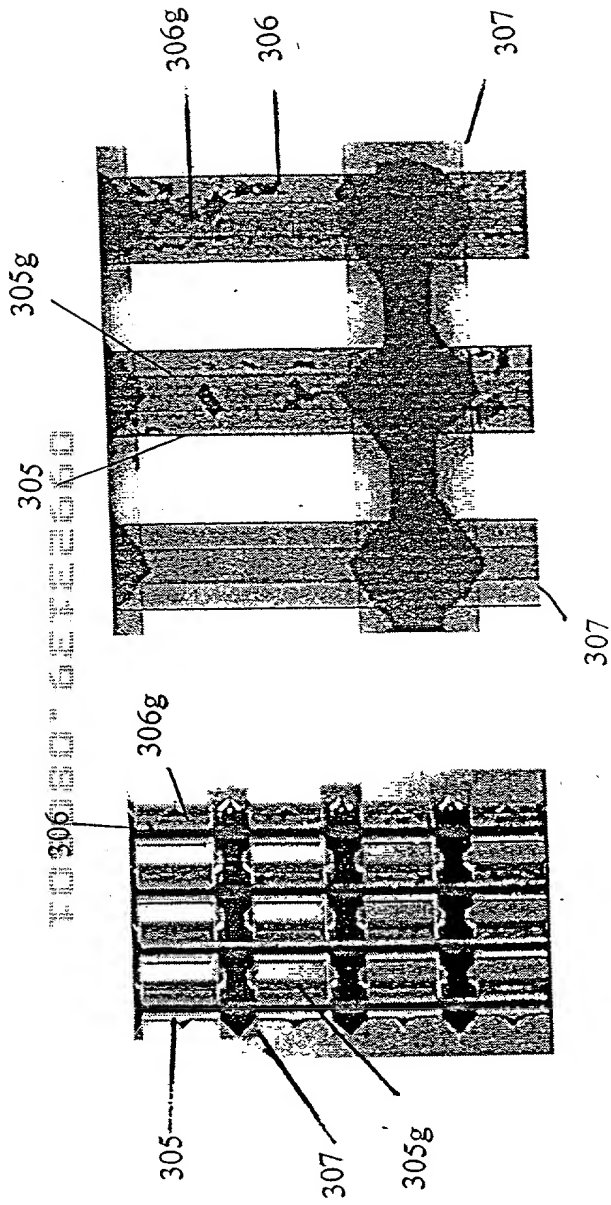


FIGURE 12(a)

FIGURE 12(b)

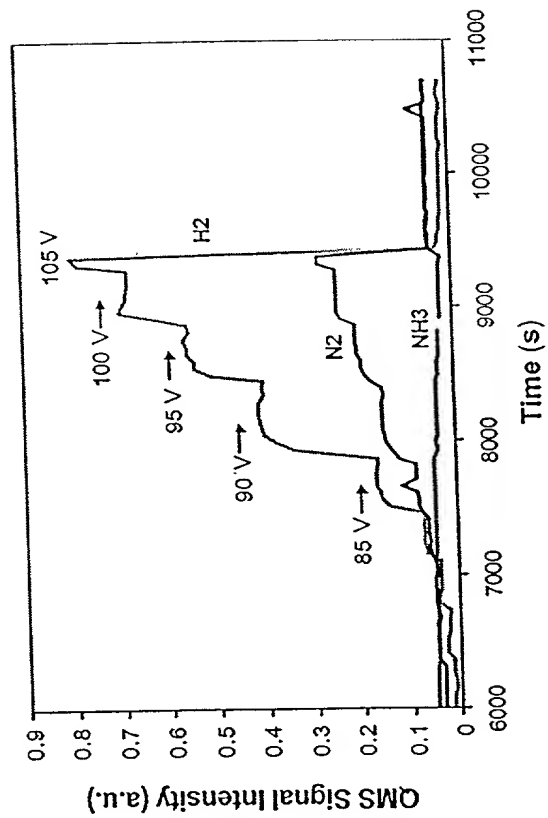


FIGURE 12(c)

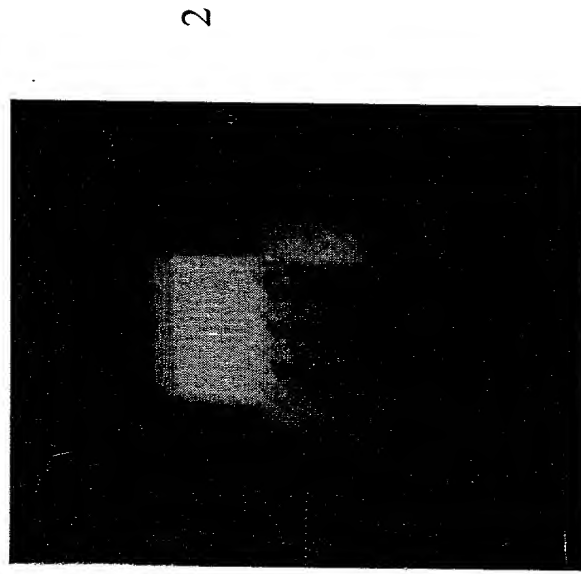
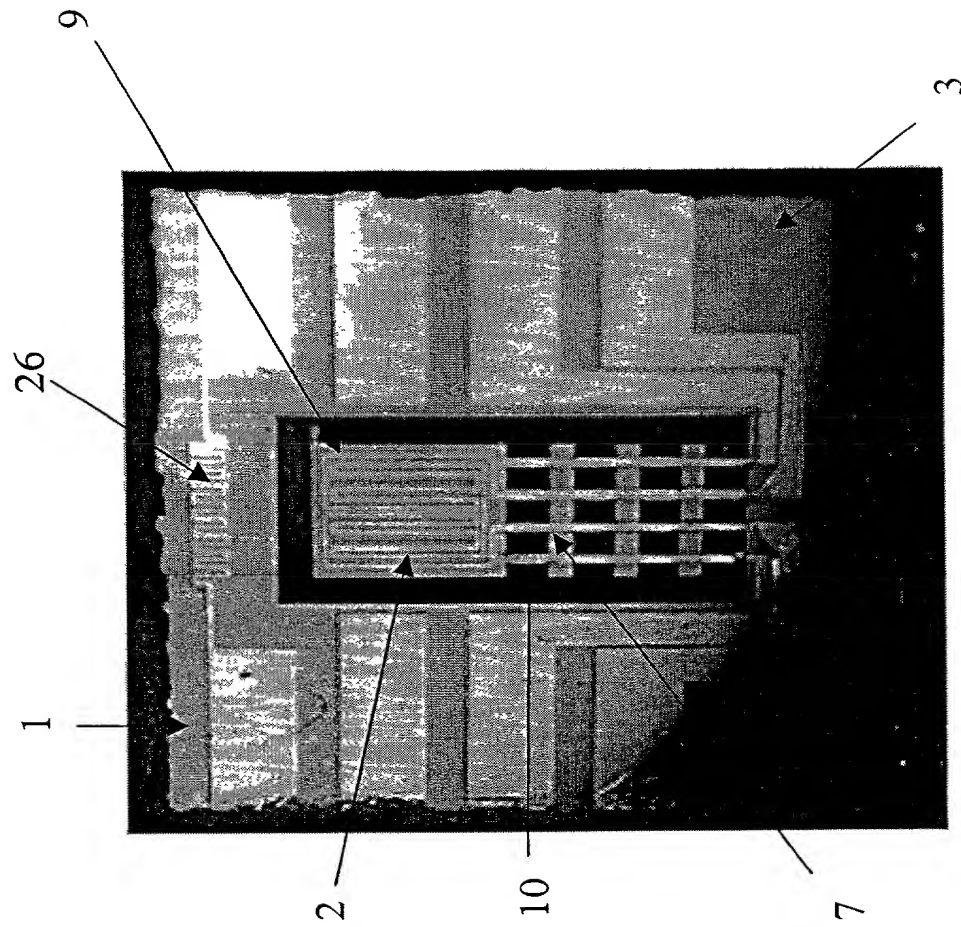


FIGURE 13(b)

FIGURE 13(a)